



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE::MADANAPALLE
(UGC AUTONOMOUS)

www.mits.ac.in

(Affiliated to JNTUA, Anantapur & Approved by AICTE, New Delhi)
An ISO 9001: 2008 Certified Institution

M.Tech. I Semester I Mid –Term Test Schedule - November – 2017

EXAM SECTION – NOTIFICATION DATED: 20.11.2017

M.Tech. I Semester (2017 Admitted Batch)

Academic Year – 2017 - 18

TIMINGS: FN: 10:30 AM to 12.30 PM
AN: 02:30 PM to 04:30 PM

DATE / DAY		Structural Engineering (CE)	Electrical Power Systems (EPS)	Advanced Manufacturing Systems (AMS)	Computer Science & Engineering (CSE)
29.11.2017 (Wednesday)	FN	Higher Engineering Mathematics 17SE101	Modern Control Theory 16EPS101	Automation in Manufacturing 16AMS101	Advanced Operating Systems 16CSE101
	AN	Advanced Structural Analysis 17SE102	Power System Dynamics and Stability 16EPS102	Advanced Manufacturing Processes 16AMS102	Advanced Data Structures and Algorithms 16CSE102
30.11.2017 (Thursday)	FN	Theory of Elasticity and Plasticity 17SE103	Modern Power System Analysis 16EPS103	Material Technology 16AMS103	Computer System Design 16CSE103
	AN	Structural Dynamics 17SE104	EHVAC Transmission 16EPS104	Product Design & Development 16AMS104	Advanced Computer Networks 16CSE104
02.12.2017 (Saturday)	FN	Advanced Steel Structures 17SE105	Analysis of Power Electronic Converters 16EPS105	Theory of Metal Cutting & Tool Design 16AMS105	Object Oriented Analysis & Design Patterns 16CSE105
	AN	Advanced Concrete Technology 17SE202	Artificial Intelligence Techniques 16EPS401	Simulation & Modeling of Manufacturing Systems 16AMS404	Software Engineering 16CSE401

Note: All HODs are requested to circulate among the faculty in the department.


CONTROLLER OF EXAMINATIONS


PRINCIPAL
Etc

Copy to:

1. All HODs : EEE ME ECE CSE

EC To:

2. SAO(G) SAO (A&P) S.P.O AO(Systems) SWO Dean(R&D) Dean(PGP) TC Librarian SO TI Dy.Warden

3. Copy to HOD's to be read in M.Tech I Semester Class Rooms 4. Department & Academic Section Notice Boards